

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: Unassigned Examiner: Unassigned

In Re PATENT APPLICATION Of:

Applicant : Masaru TAKAISHI)

Serial No. : 10/766,212)

Filed : January 29, 2004) DISCLOSURE)

For : METHOD FOR MANUFACTURING)

SEMICONDUCTOR DEVICE)

Attorney Ref. : AI 325)_______ April 27, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an Information Disclosure Statement submitted in compliance with the timing requirements of 37 C.F.R. § 1.97(b)(1), i.e. within three months of the filing date of the application.

Attached is an English-language translation of the Japanese patent publication No. 2003-303785. The publication was previously submitted in the United States Patent and Trademark Office on January 29, 2004, with the Information Disclosure Statement filed with the application. Any further relevance of the publication may be gleaned from its translation. Also attached is Form PTO-1449 listing the publication and its translation.

Since this Information Disclosure Statement is being filed within three months of the filing date of the application, no certification or fee is required, and the requirements of 37 C.F.R. §§ 1.97 and 1.98 are deemed to be fully met as to all listed documents. Consideration of the listed document is respectfully requested.

April 27, 2004

Date

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Respectfully submitted

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FEE ENCLOSED:\$ Please charge any further fee to our Deposit Account No. 18-0002

RHB:vm



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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty. Docket	Application	Application No.		
				AI 325	10/	10/766,212		
				Applicant Masaru TAKAISHI				
				Filing Date January 29, 2004	Group Una	Group Unassigned		
				U.S. PATENT DOCUMENTS				
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